



Sri Vasavi Engineering College

Pedatadepalli, Tadepalligudem

(Approved by AICTE, Permanently Affiliated to JNTUK, Kakinada
& Accredited with NAAC 'A' Grade)

Electro Nivedika

- Voice of ECE

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DEPARTMENT OF ECE

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Events conducted in the department

Engineer's Day Celebrations

Panchatantra, an Intra-collegiate fest was conducted on the occasion of Engineer's day celebrations to motivate students to improve their intra-personal and inter personal skills covering both technical and non-technical competitions



Faculty Corner

Publications in Journals/ Conferences

| S. No | Name of the Staff Member | Title of the Paper | Name of the Journal & Year of Publication | Impact Factor |
|-------|--|---|---|---------------|
| 1 | Dr. E. Kusuma Kumari, Professor | Design of Rectangular patch antenna array for wireless communications | International Journal of Engineering and Science Research (IJESR), Nov.2015/Vol5/Issue11/ 14201427, ISSN:2277-2685 | 3.47 |
| | | Switched Beam Reconfigurable Micro strip Patch antenna for Wi-Max application | International Journal of Advanced Research in Electronics & Communication Engineering (IJARECE) ISSN:2278-909X Volume-4, Issue 11, Nov 2015 | 4.295 |
| 2 | Sri. D. Rajesh Setty, Assoc. Professor & Ms. B. Sai Prasanthi, Asst. Professor | Paper Currency Authentication System-A Quick Authentication System | International Journal of Scientific & Engineering Research, ISSN 2229-5518 Volume No: 6 Issue: 9, September-2015 Page No: 1249-1256 | 3.8 |
| | | Indian Paper Currency Authentication System using Image Processing | International Journal of Scientific Research Engineering & Technology (IJSRET), ISSN 2278 08 Volume 4, Issue 9, September 2015 | 1.24 |
| 3 | Sri. M. Koteswararao, Assoc. Professor | Novel Shadow Based Hybrid Adaptive Filter Algorithm for Speech Signal | CIIT international journal of digital signal processing, vol 7, no 08, October November 2015 | 4.40 |
| | | Speech Processing through Shadow based Adaptive Filter | International Journal of Applied Engineering Research ISSN:0973-4562 Volume 10, November 12, 2015 pp.32223-32232 | - |



Workshops/ Seminars / FDPs attended

| S. No | Name of the Staff Member | Title of the Paper | Name of the Journal & Year of Publication | Impact Factor |
|-------|--|---|--|---------------|
| 4 | Sri. M. Kishore Kumar, Assoc. Professor | Implementation of energy efficient resource allocation for OFDM- based cognitive radio networks | International Journal of Science and Information Technologies, ISSN:0975-9646, Vol-6(5), Sep-Oct, 2015 | 3.32 |
| | | Design of Rectangular patch antenna array for wireless communications | International Journal of Engineering and Science Research (IJESR), Nov.2015/Vol-5/Issue-11/1420-1427, ISSN 2277-2685 | - |
| 5 | Sri N.V. Narayana Rao, Sr. Asst. Professor | Implementation of Video Surveillance Using Raspberry P12 | International Journal and Magazine of Engineering, Technology, Management and Research, ISSN2348-4845, October 2015 | 2.625 |
| 6 | Smt. Y. Sujatha, Sr. Asst. Professor | Design of a 64 bit Quantum Comparator using Reversible Logic | International Journal of VLSI System Design and Communication System ISSN:2322- 0929 Volume3, Issue8,October-2015 | 3.014 |
| 7 | Sri. N.V.N. Prasanna Kumar Asst. Professor | Implementation of Double Precision Floating Point Adder with Residue for High Accuracy Using FPGA | International Journal of Scientific Research Engineering & Technology(IJSRET), ISSN 22 - 0882 Volume 4, Issue 10, October 2015 | 3.241 |

| S. No | Name of the Faculty | FDP/ Work Shop | Venue | Date/ Year |
|-------|---|---|--|--|
| 1 | Sri. P. Murali Krishna Asst. Professor & Sri KSS Kiran Asst. Professor | Low Power System On Chip Design using XILINX FPGA Devices | National Institute of Technology Warangal | 3 rd -7 th October, 2015 |
| 2 | Ms. Ch.Jyotsna Rani Asst. Professor Ms. T.N.V. Prathyusha Asst. Professor Ms. V.Radha Asst. Professor | Analog and Digital CMOS IC Design Using Mentor Graphics EDA Tools | Shri Vishnu Engineering College for Women Bhimavaram | 5 th -6 th October, 2015 |

